

CMOS Triple 2-Channel Analog Multiplexer/Demultiplexer

1 FEATURES

- **-3dB Bandwidth: 180MHz**
- **Single Supply Operation +2.5V to +5.5V**
- **Low ON Resistance: 48Ω(TYP) With 5V Supply**
- **High Off-Isolation: -83dB ($R_L = 50\Omega$, $f = 1\text{MHz}$)**
- **Break-Before-Make Switching**
- **Binary Address Decoding on Chip**
- **Operating Temperature Range: -40°C to +125°C**
- **PACKAGES: SOIC16**

2 APPLICATIONS

- **Sensors**
- **Analog and Digital Multiplexing and Demultiplexing**
- **A/D and D/A Conversion**
- **Signal Gating**
- **Battery-Operated Equipment**
- **Factory Automation**
- **Appliances**
- **Communications Circuits**

3 DESCRIPTIONS

The RES74LV4053 is a CMOS analog IC configured as three single-pole/double-throw (SPDT) switches. This CMOS device can operate from 2.5 V to 5.5 V.

The RES74LV4053 device are digitally controlled analog switches. It has low on-resistance (48Ω TYP) and very low off-leakage current (1nA TYP).

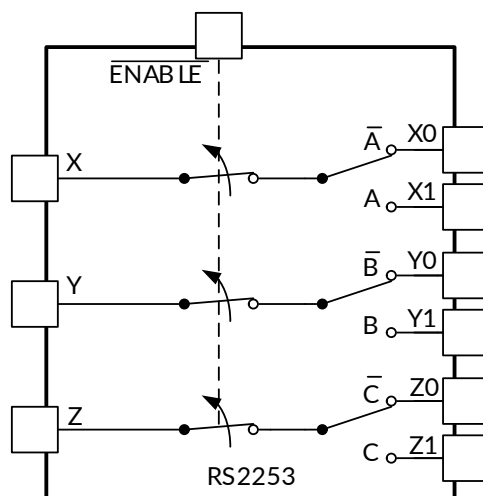
The RES74LV4053 is available in Green SOIC16 packages. It operates over an ambient temperature range of -40°C to +125°C.

Device Information ⁽¹⁾

PART NUMBER	PACKAGE	BODY SIZE (NOM)
RES74LV4053CSR	SOIC16	9.90mm×3.91mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

4 Functional Diagrams of RES74LV4053



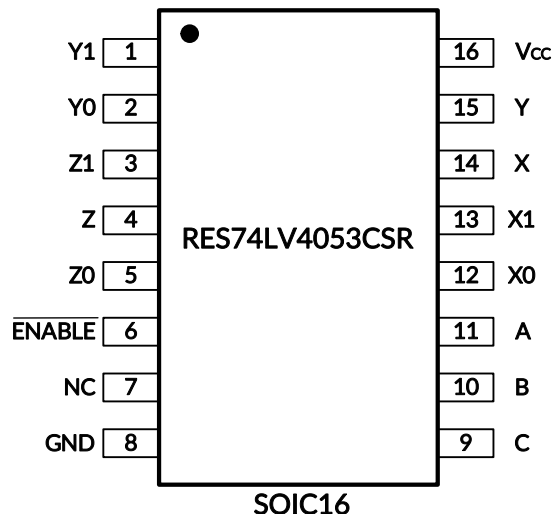
6 PACKAGE/ORDERING INFORMATION ⁽¹⁾

PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING ⁽²⁾	MSL ⁽³⁾	PACKAGE OPTION
RES74LV4053						
	RES74LV4053CSR	-40°C ~+125°C	SOIC16	RES74LV4053CSR	MSL3	Tape and Reel,4000

NOTE:

- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.
- (2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.
- (3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.

7 Pin Configurations (Top View)



7.1 PIN DESCRIPTION

NAME	RES74LV4053CSR	FUNCTION
	SOIC16	
Y1	1	Analog Switch "Y" Normally Open Input or Output.
Y0	2	Analog Switch "Y" Normally Closed Input or Output.
Z1	3	Analog Switch "Z" Normally Open Input or Output.
Z	4	Analog Switch "Z" Input or Output.
Z0	5	Analog Switch "Z" Normally Closed Input or Output.
ENABLE	6	Digital Enable Input. Normally connected to GND.
NC	7	No Connect.
GND	8	Ground. Connect to digital ground.
C	9	Digital Address "C" Input.
B	10	Digital Address "B" Input.
A	11	Digital Address "A" Input.
X0	12	Analog Switch "X" Normally Closed Input or Output.
X1	13	Analog Switch "X" Normally Open Input or Output.
X	14	Analog Switch "X" Input or Output.
Y	15	Analog Switch "Y" Input or Output.
Vcc	16	Positive Analog and Digital Supply Voltage Input

7.2 FUNCTION TABLE

ENABLE INPUT	INPUT STATES			ON CHANNEL(S)
	C	B	A	
1	X	X	X	NONE
0	0	0	0	X0, Y0, Z0
0	0	0	1	X1, Y0, Z0
0	0	1	0	X0, Y1, Z0
0	0	1	1	X1, Y1, Z0
0	1	0	0	X0, Y0, Z1
0	1	0	1	X1, Y0, Z1
0	1	1	0	X0, Y1, Z1
0	1	1	1	X1, Y1, Z1

X=Don't care

NOTE: Input and output pins are identical and inter-changeable. Either may be considered an input or output; signals pass equally well in either direction.

8 SPECIFICATIONS

8.1 Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) ⁽¹⁾

SYMBOL	PARAMETER		MIN	MAX	UNIT
V _{CC}	Supply Voltage		-0.3	6	V
V _{IN}	Input Voltage (All inputs)		-0.3	V _{CC} +0.3	
I _{IN}	Switch Input Current	Any one input	-20	+20	mA
I _{PEAK}	Peak Switch Current	Pulsed at 1ms Duration, <10% Duty Cycle	-40	+40	
θ _{JA}	Package thermal impedance ⁽²⁾				°C/W
		SOIC6		110	
T _J	Junction Temperature ⁽³⁾		-40	150	°C
T _{stg}	Storage temperature		-65	+150	

(1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied.

(2) The package thermal impedance is calculated in accordance with JESD-51.

(3) The maximum power dissipation is a function of T_{J(MAX)}, R_{θJA}, and T_A. The maximum allowable power dissipation at any ambient temperature is P_D = (T_{J(MAX)} - T_A) / R_{θJA}. All numbers apply for packages soldered directly onto a PCB.

8.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

			VALUE	UNIT
V _(ESD)	Electrostatic discharge	Human-body model (HBM)	±3000	V
		Machine Model (MM)	±200	V



ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

8.3 Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted)

SYMBOL	PARAMETER	MIN	MAX	UNIT
V _{CC}	Supply Voltage	2.5	5.5	V
T _A	Operating temperature	-40	+125	°C

8.4 ELECTRICAL CHARACTERISTICS

$V_{CC} = 5.0\text{ V}$ or 3.3 V , FULL = -40°C to $+125^{\circ}\text{C}$, Typical values are at $T_A = +25^{\circ}\text{C}$. (unless otherwise noted)

PARAMETER	SYMBOL	CONDITIONS	V _{CC}	T _A	MIN ⁽²⁾	TYP ⁽³⁾	MAX ⁽²⁾	UNIT
ANALOG SWITCH								
Analog Signal Range	V _{X_} , V _{Y_} , V _{Z_} V _X , V _Y , V _Z			FULL	GND		V _{CC}	V
On-Resistance	R _{ON}	V _{CC} =5V, I _X , I _Y , I _Z =1mA	5V	+25°C		48	65	Ω
				FULL			70	Ω
		V _{CC} =3.3V, I _X , I _Y , I _Z =1mA	3.3V	+25°C		100	130	Ω
				FULL			140	Ω
On-Resistance Match Between Channels	ΔR _{ON}	V _{CC} =5V, I _X , I _Y , I _Z =1mA	5V	+25°C		1.5	5	Ω
				FULL			5.3	Ω
On-Resistance Flatness	R _{FLAT(ON)}	V _{CC} =5V, I _X , I _Y , I _Z =1mA	5V	+25°C		17	25	Ω
				FULL			28	Ω
X ₋ , Y ₋ , Z ₋ Off Leakage Current	I _{X(OFF)} I _{Y(OFF)} I _{Z(OFF)}	V _{X₋} , V _{Y₋} , V _{Z₋} =1V, 4.5V V _X , V _Y , V _Z =4.5V, 1V	5V	+25°C		1	100	nA
		V _{X₋} , V _{Y₋} , V _{Z₋} =1V, 3V V _X , V _Y , V _Z =3V, 1V	3.3V	+25°C		1	100	nA
X, Y, Z Off Leakage Current	I _{X(OFF)} I _{Y(OFF)} I _{Z(OFF)}	V _{X₋} , V _{Y₋} , V _{Z₋} =1V, 4.5V V _X , V _Y , V _Z =4.5V, 1V	5V	+25°C		1	100	nA
		V _{X₋} , V _{Y₋} , V _{Z₋} =1V, 3V V _X , V _Y , V _Z =3V, 1V	3.3V	+25°C		1	100	nA
X, Y, Z On Leakage Current	I _{X(ON)} I _{Y(ON)} I _{Z(ON)}	V _{CC} =5V, V _X , V _Y , V _Z =4.5V, 1V	5V	+25°C		1	100	nA
		V _{CC} =3.3V, V _X , V _Y , V _Z =3V, 1V	3.3V	+25°C		1	100	nA
DIGITAL CONTROL INPUTS ⁽¹⁾								
Logic Input Logic Threshold High	V _{AH} , V _{BH} , V _{CH} , V _{ENABLE}		5V	+25°C	1.7			V
			3.3V	+25°C	1.7			V
Logic Input Logic Threshold Low	V _{AL} , V _{BL} , V _{CL} , V _{ENABLE}		5V	+25°C			0.5	V
			3.3V	+25°C			0.5	V
Input-Current High	I _{AH} , I _{BH} , I _{CH} I _{ENABLE}	V _A , V _B , V _C , V _{ENABLE} = V _{CC}	3.3V to 5V	+25°C		1	100	nA
Input-Current Low	I _{AL} , I _{BL} , I _{CL} I _{ENABLE}	V _A , V _B , V _C , V _{ENABLE} = 0V	3.3V to 5V	+25°C		1	100	nA

(1) All unused digital inputs of the device must be held at VIO or GND to ensure proper device operation.

(2) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.

(3) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.

10 Parameter Measurement Information

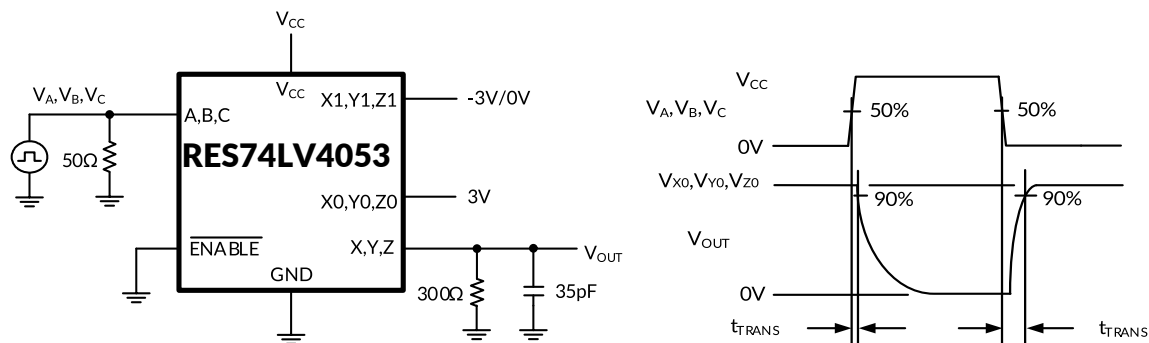


Figure 2. Address Transition Times (t_{TRANS})

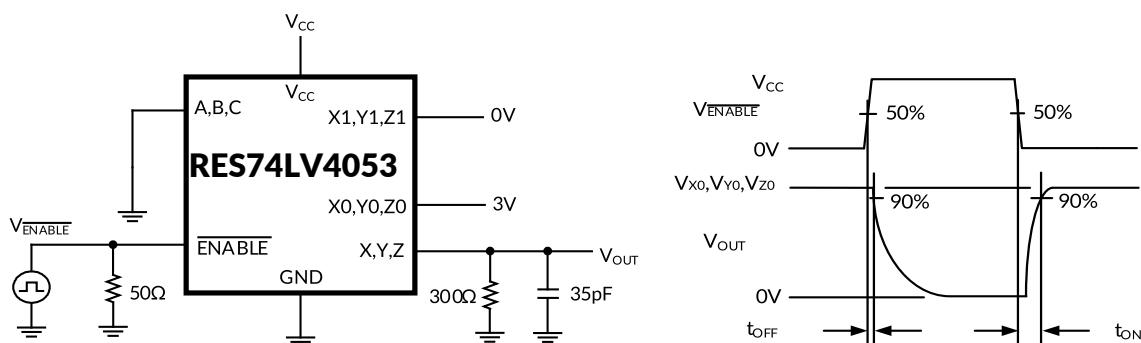


Figure 3. Switching Times (t_{ON} , t_{OFF})

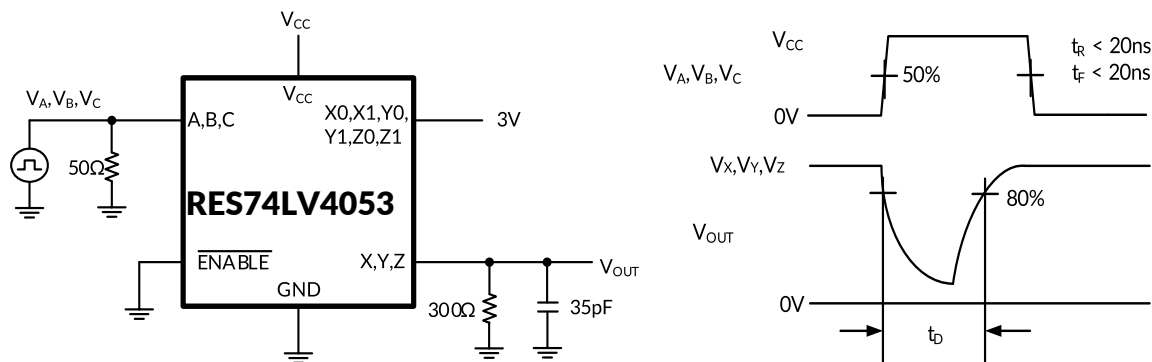


Figure 4. Break-Before-Make Time Delay (t_D)

Parameter Measurement Information (continued)

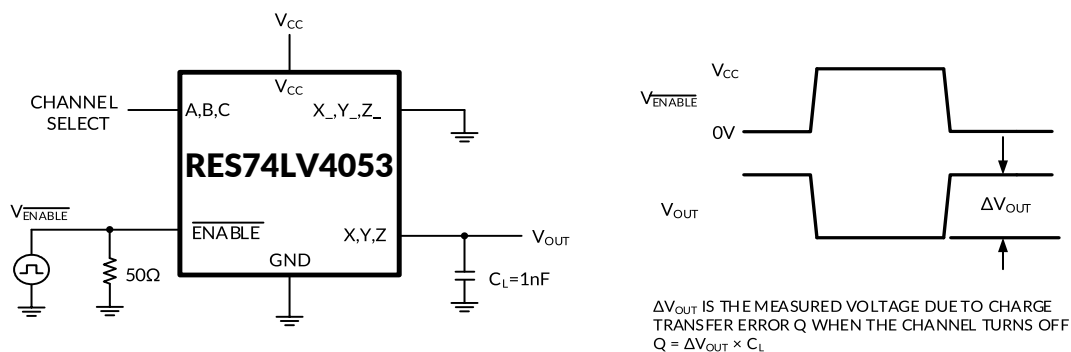
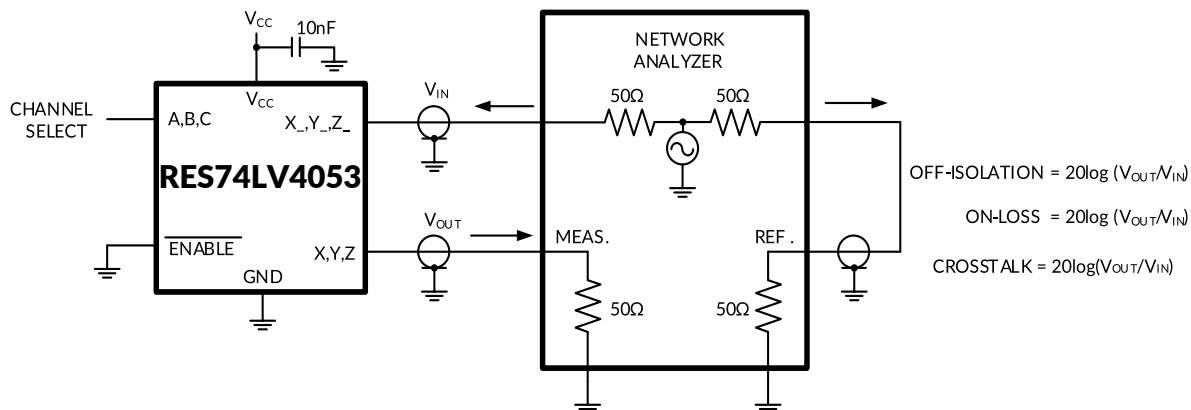


Figure 5. Charge Injection (Q)



MEASUREMENTS ARE STANDARDIZED AGAINST SHORT AT SOCKET TERMINALS .
 OFF-ISOLATION IS MEASURED BETWEEN COM AND "OFF" NO TERMINAL ON EACH SWITCH.
 ON-LOSS IS MEASURED BETWEEN COM AND "ON" NO TERMINAL ON EACH SWITCH.
 CROSSTALK IS MEASURED FROM ONE CHANNEL(A,B,C) TO ALL OTHER CHANNELS.
 SIGNAL DIRECTION THROUGH SWITCH IS REVERSED ; WORST VALUES ARE RECORDED .

Figure 6. Off Isolation, On Loss

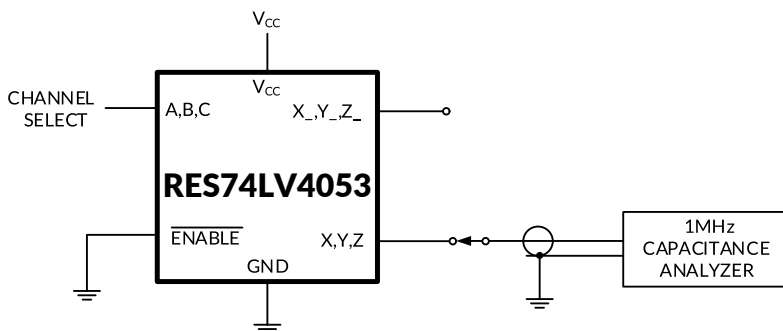


Figure 7. Capacitance

11 APPLICATION NOTES

The RES74LV4053 device is a tri-state 2-channel multiplexer having three separate digital control inputs, A, B, and C, and an inhibit input. Each control input selects one of a pair of channels which are connected in a single-pole, double-throw configuration.

When the devices are used as demultiplexers, the CHANNEL IN/OUT terminals are the outputs and the COMMON OUT/IN terminals are the inputs.

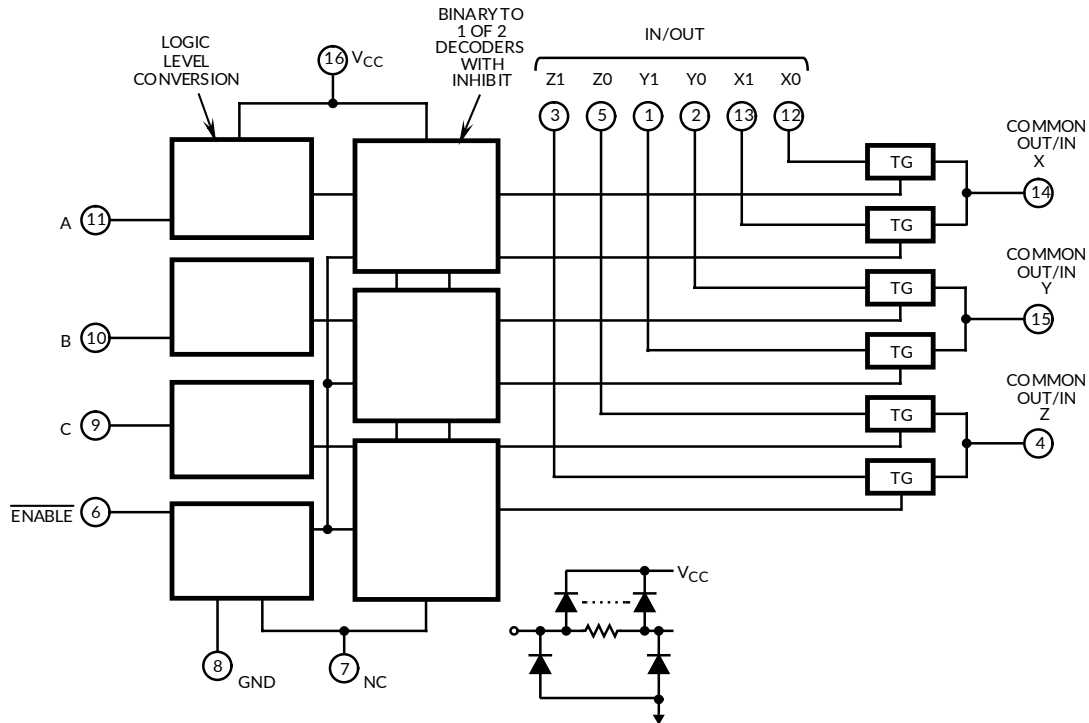
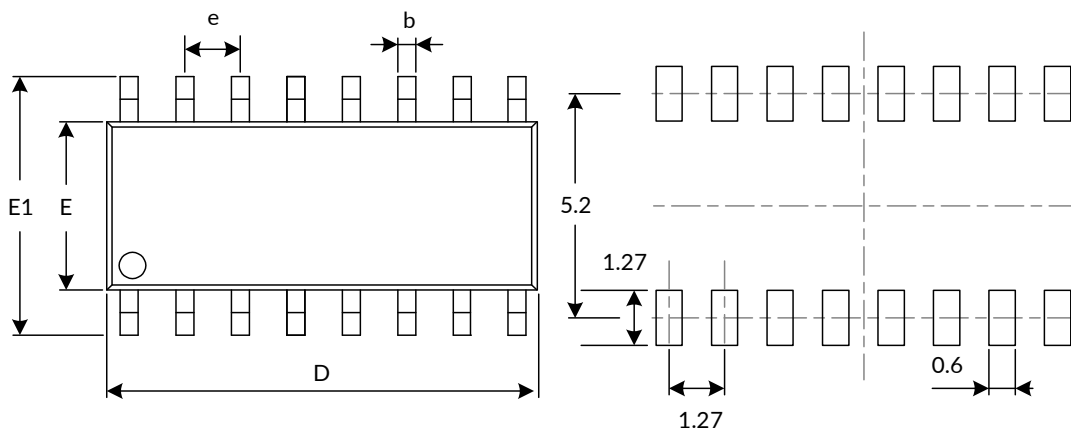


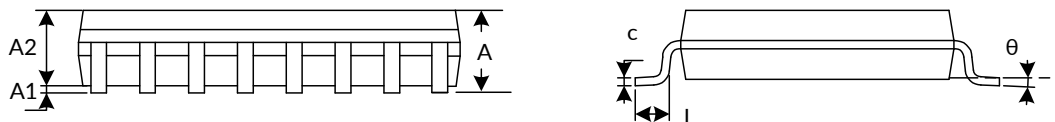
Figure 8. The RES74LV4053 Functional Block Diagram

12 PACKAGE OUTLINE DIMENSIONS

SOIC16⁽³⁾



RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A ⁽¹⁾	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D ⁽¹⁾	9.800	10.200	0.386	0.402
E ⁽¹⁾	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.27(BSC) ⁽²⁾		0.050(BSC) ⁽²⁾	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

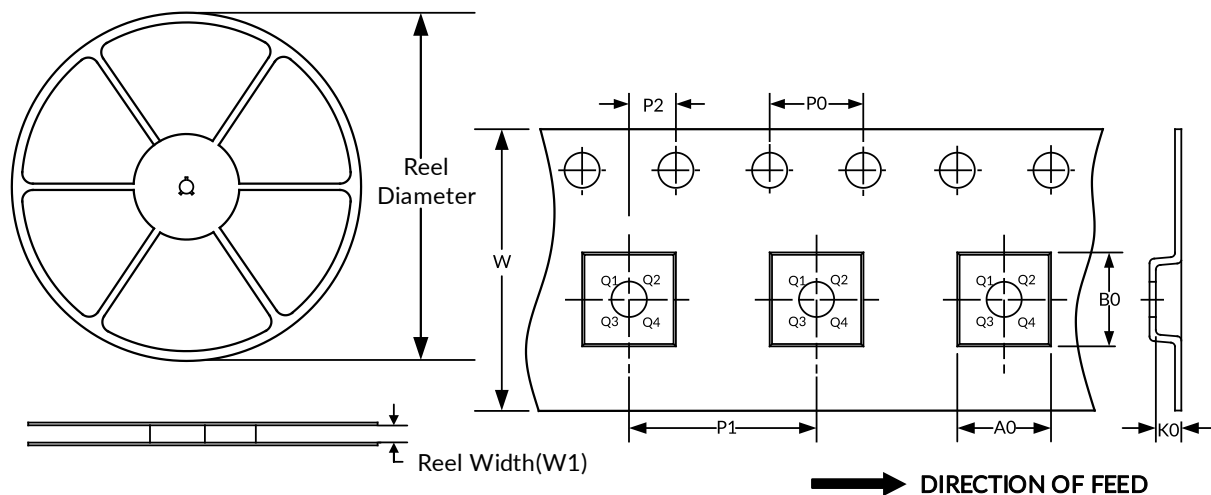
NOTE:

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.

13 TAPE AND REEL INFORMATION

REEL DIMENSIONS

TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width(mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOIC16	13"	12.4	8.30	6.70	2.10	4.0	8.0	2.0	12.0	Q1

NOTE:

1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.